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IEE CNF	IEE Conference Proceeding
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1. **An integrated CAD system for algorithm-specific IC design**
Shung, C.B.; Jain, R.; Rimey, K.; Wang, E.; Srivastava, M.B.; Richards, B.C.; L
Azim, S.; Thon, L.; Hilfinger, P.N.; Rabaey, J.M.; Brodersen, R.W.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction
Volume 10, Issue 4, April 1991 Page(s):447 - 463
Digital Object Identifier 10.1109/43.75628
AbstractPlus | Full Text: PDF(2116 KB) IEEE JNL

2. **Mixing simulations and real subsystems for subsea robot development. S development of the core simulation engine**
Lane, D.M.; Falconer, G.J.; Randall, G.W.; Duffy, N.D.; Herd, J.T.; Chernett, P.
Colley, M.; Standeven, J.; Callaghan, V.; Smith, J.; Evans, J.; Woods, A.; Penr
G.A.; Smith, D.; Edwards, I.;
OCEANS '98 Conference Proceedings
Volume 3, 28 Sept.-1 Oct. 1998 Page(s):1382 - 1386 vol.3
Digital Object Identifier 10.1109/OCEANS.1998.726295
AbstractPlus | Full Text: PDF(656 KB) IEEE CNF

3. **A technique for designing robotic control systems based on Petri nets**
Caloini, A.; Magnani, G.; Pezze, M.;
Control Systems Technology, IEEE Transactions on
Volume 6, Issue 1, Jan. 1998 Page(s):72 - 87
Digital Object Identifier 10.1109/87.654878
AbstractPlus | References | Full Text: PDF(440 KB) IEEE JNL

4. **The ADAM design planning engine**
Knapp, D.W.; Parker, A.C.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction
Volume 10, Issue 7, July 1991 Page(s):829 - 846
Digital Object Identifier 10.1109/43.87595
AbstractPlus | Full Text: PDF(1712 KB) IEEE JNL

5. **1999 Index IEEE transactions on systems, man, and cybernetics Part A: s humans Vol. 29**
Systems, Man and Cybernetics, Part A, IEEE Transactions on
Volume 29, Issue 6, Nov. 1999 Page(s):1 - 11
Digital Object Identifier 10.1109/TSMCA.1999.798811
AbstractPlus | Full Text: PDF(248 KB) IEEE JNL

6. **Composite objects: real-time programming with CORBA**
Polze, A.; Sha, L.;

Euromicro Conference, 1998. Proceedings. 24th
Volume 2, 25-27 Aug. 1998 Page(s):997 - 1004 vol.2
Digital Object Identifier 10.1109/EURMIC.1998.708133

AbstractPlus | Full Text: PDF(732 KB) IEEE CNF

7. **Real-time detection and tracking of a moving object using a complex pro device**
Benkhalil, A.K.; Sipson, S.S.; Booth, W.;
Target Tracking and Data Fusion (Digest No. 1998/282), IEE Colloquium on
9 June 1998 Page(s):10/1 - 10/7

AbstractPlus | Full Text: PDF(436 KB) IEE CNF

8. **The role of process abstraction in simulation**
Fishwick, P.A.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 18, Issue 1, Jan.-Feb. 1988 Page(s):18 - 39
Digital Object Identifier 10.1109/21.87052

AbstractPlus | Full Text: PDF(2004 KB) IEEE JNL

9. **1995 Index IEEE Transactions on Software Engineering Vol. 21**
Software Engineering, IEEE Transactions on
Volume 21, Issue 12, Dec. 1995 Page(s):1
Digital Object Identifier 10.1109/TSE.1995.489077

AbstractPlus | Full Text: PDF(960 KB) IEEE JNL

10. **A data flow architecture for parallel computation**
Abellard, P.; Nolibe, G.; Razafindrakoto, N.;
Databases, Parallel Architectures and Their Applications,. PARBASE-90, Inter Conference on
7-9 March 1990 Page(s):129 - 131
Digital Object Identifier 10.1109/PARBSE.1990.77130

AbstractPlus | Full Text: PDF(228 KB) IEEE CNF

11. **Managing complexity of modeling industrial processes with P/T nets**
Uthmann, C.V.; Becker, J.;
Systems, Man, and Cybernetics, 1998. 1998 IEEE International Conference or
Volume 1, 11-14 Oct 1998 Page(s):547 - 552 vol.1
Digital Object Identifier 10.1109/ICSMC.1998.725469

AbstractPlus | Full Text: PDF(608 KB) IEEE CNF

12. **Real-time DKS on a single chip**
Leung, S.; Shanblatt, M.;
Robotics and Automation, IEEE Journal of [legacy, pre - 1988]
Volume 3, Issue 4, Aug 1987 Page(s):281 - 290

AbstractPlus | Full Text: PDF(1032 KB) IEEE JNL

13. **An architecture for reflexive autonomous vehicle control**
Payton, D.;
Robotics and Automation. Proceedings. 1986 IEEE International Conference c
Volume 3, Apr 1986 Page(s):1838 - 1845

AbstractPlus | Full Text: PDF(1088 KB) IEEE CNF

14. **Interoperability and synchronisation of distributed hardware-in-the-loop : underwater robot development: issues and experiments**
Lane, D.M.; Falconer, G.J.; Randall, G.; Edwards, I.;
Robotics and Automation, 2001. Proceedings 2001 ICRA. IEEE International C
Volume 1, 2001 Page(s):909 - 914 vol.1
Digital Object Identifier 10.1109/ROBOT.2001.932666

AbstractPlus | Full Text: PDF(420 KB) IEEE CNF

15. **Parallel implementation of Newton-Euler algorithm with one step ahead p**

Yamakita, M.; Hoshino, Y.; Morimoto, K.; Furuta, K.;
Control 1991. Control '91., International Conference on
25-28 Mar 1991 Page(s):364 - 369 vol.1

AbstractPlus | Full Text: PDF(320 KB) IEE CNF

16. **Computers for symbolic processing**
Wah, B.W.; Lowrie, M.B.; Li, G.-J.;
Proceedings of the IEEE
Volume 77, Issue 4, April 1989 Page(s):509 - 540
Digital Object Identifier 10.1109/5.24142

AbstractPlus | Full Text: PDF(3060 KB) IEEE JNL

17. **Back cover**
Electron Devices, IEEE Transactions on
Volume 32, Issue 12, Dec 1985 Page(s):c4 - c4

Full Text: PDF(6424 KB) IEEE JNL

18. **Back cover**
Proceedings of the IEEE
Volume 75, Issue 12, Dec. 1987 Page(s):c4 - c4

Full Text: PDF(1994 KB) IEEE JNL

19. **A real-time reflexive pilot for an autonomous land vehicle**
Nitao, J.; Parodi, A.;
Control Systems Magazine, IEEE
Volume 6, Issue 1, Feb 1986 Page(s):14 - 23

AbstractPlus | Full Text: PDF(1000 KB) IEEE JNL

20. **Extending CAD tools and techniques**
De Micheli, G.;
Computer
Volume 26, Issue 1, Jan. 1993 Page(s):85 - 87
Digital Object Identifier 10.1109/2.179166

AbstractPlus | Full Text: PDF(368 KB) IEEE JNL

21. **Hardware-software co-design of embedded systems [and prolog]**
Wolf, W.H.;
Proceedings of the IEEE
Volume 82, Issue 7, July 1994 Page(s):967 - 989
Digital Object Identifier 10.1109/5.293155

AbstractPlus | Full Text: PDF(2392 KB) IEEE JNL

22. **System level hardware module generation**
Srivastava, M.B.; Brodersen, R.W.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 3, Issue 1, March 1995 Page(s):20 - 35
Digital Object Identifier 10.1109/92.365451

AbstractPlus | Full Text: PDF(1732 KB) IEEE JNL

23. **Design and control of a force-reflecting teleoperation system with magne master and wrist**
Salcudean, S.E.; Wong, N.M.; Hollis, R.L.;
Robotics and Automation, IEEE Transactions on
Volume 11, Issue 6, Dec. 1995 Page(s):844 - 858
Digital Object Identifier 10.1109/70.478431

AbstractPlus | Full Text: PDF(1940 KB) IEEE JNL

24. **VLSI architectures for high-speed range estimation**
Sastry, R.; Ranganathan, N.; Jain, R.C.;
Pattern Analysis and Machine Intelligence, IEEE Transactions on
Volume 17, Issue 9, Sept. 1995 Page(s):894 - 899

Digital Object Identifier 10.1109/34.406655

AbstractPlus | References | Full Text: PDF(564 KB) IEEE JNL

25. **SIERA: a unified framework for rapid-prototyping of system-level hardwa**
Srivastava, M.; Brodersen, R.W.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction
Volume 14, Issue 6, June 1995 Page(s):676 - 693
Digital Object Identifier 10.1109/43.387729

AbstractPlus | Full Text: PDF(1876 KB) IEEE JNL

26. **From real-time emulation to ASIC integration for image processing appli**
Kraljic, I.C.; Quenot, G.M.; Zavidovique, B.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 4, Issue 3, Sept. 1996 Page(s):391 - 404
Digital Object Identifier 10.1109/92.532039

AbstractPlus | References | Full Text: PDF(1600 KB) IEEE JNL

27. **1996 Index Proceedings of the IEEE Vol. 82-84**
Proceedings of the IEEE
Volume 84, Issue 12, Dec. 1996 Page(s):0_5
Digital Object Identifier 10.1109/JPROC.1996.546442

AbstractPlus | Full Text: PDF(5516 KB) IEEE JNL

28. **Hardware/software co-design**
De Michell, G.; Gupta, R.K.;
Proceedings of the IEEE
Volume 85, Issue 3, March 1997 Page(s):349 - 365
Digital Object Identifier 10.1109/5.558708

AbstractPlus | References | Full Text: PDF(252 KB) IEEE JNL

29. **1998 Index Proceedings Of The Ieee Vols. 84-86**
Proceedings of the IEEE
Volume 86, Issue 12, Dec. 1998 Page(s):0_3 - 0_53
Digital Object Identifier 10.1109/JPROC.1998.735524

AbstractPlus | Full Text: PDF(752 KB) IEEE JNL

30. **Subject Index**
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transacti
Circuits and Systems II: Express Briefs, IEEE Transactions on]
Volume 45, Issue 12, Dec. 1998 Page(s):1623 - 1639
Digital Object Identifier 10.1109/TCSII.1998.746689

AbstractPlus | Full Text: PDF(320 KB) IEEE JNL

31. **Designing and programming the emotion engine**
Oka, M.; Suzuoki, M.;
Micro, IEEE
Volume 19, Issue 6, Nov.-Dec. 1999 Page(s):20 - 28
Digital Object Identifier 10.1109/40.809374

AbstractPlus | References | Full Text: PDF(472 KB) IEEE JNL

32. **Subject Index**
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transacti
Circuits and Systems II: Express Briefs, IEEE Transactions on]
Volume 47, Issue 12, Dec. 2000 Page(s):1596 - 1615
Digital Object Identifier 10.1109/TCSII.2000.899663

AbstractPlus | Full Text: PDF(168 KB) IEEE JNL

33. **A three-dimensional human agent metaphor for modeling and simulation**
Hopkins, J.F.; Fishwick, P.A.;
Proceedings of the IEEE
Volume 89, Issue 2, Feb 2001 Page(s):131 - 147

Digital Object Identifier 10.1109/5.910850

AbstractPlus | References | Full Text: PDF(268 KB) | Full Text: HTML IEEE

34. **Applying the design structure matrix to system decomposition and integ a review and new directions**
Browning, T.R.;
Engineering Management, IEEE Transactions on
Volume 48, Issue 3, Aug. 2001 Page(s):292 - 306
Digital Object Identifier 10.1109/17.946528

AbstractPlus | References | Full Text: PDF(528 KB) IEEE JNL

35. **Subject Index**
Proceedings of the IEEE
Volume 89, Issue 12, Dec. 2001 Page(s):1864 - 1902
Digital Object Identifier 10.1109/JPROC.2001.975951

AbstractPlus | Full Text: PDF(282 KB) IEEE JNL

36. **Enhancement of the inertial navigation system for the Morpheus autonor vehicles**
Grenon, G.; An, P.E.; Smith, S.M.; Healey, A.J.;
Oceanic Engineering, IEEE Journal of
Volume 26, Issue 4, Oct. 2001 Page(s):548 - 560
Digital Object Identifier 10.1109/48.972091

AbstractPlus | References | Full Text: PDF(506 KB) IEEE JNL

37. **The software development process for an intelligent robot**
Seward, D.W.; Garman, A.;
Computing & Control Engineering Journal
Volume 7, Issue 2, April 1996 Page(s):86 - 92

AbstractPlus | Full Text: PDF(1000 KB) IEE JNL

38. **Autosub: an autonomous unmanned submersible for ocean data collecti**
Collar, P.G.; McPhail, S.D.;
Electronics & Communication Engineering Journal
Volume 7, Issue 3, June 1995 Page(s):105 - 114

AbstractPlus | Full Text: PDF(688 KB) IEE JNL

39. **Generative feature-based design-by-constraints as a means of integratio manufacturing industry**
Jaques, M.W.S.; Billingsley, J.; Harrison, D.;
Computer-Aided Engineering Journal
Volume 8, Issue 6, Dec. 1991 Page(s):261 - 267

AbstractPlus | Full Text: PDF(464 KB) IEE JNL

40. **Condition monitoring with controlled repair capabilities for process auto welding services**
Starke, G.; Bach, C.;
Industrial Electronics Society, 1998. IECON '98. Proceedings of the 24th Annu the IEEE
Volume 4, 31 Aug.-4 Sept. 1998 Page(s):2355 - 2360 vol.4
Digital Object Identifier 10.1109/IECON.1998.724091

AbstractPlus | Full Text: PDF(796 KB) IEEE CNF

41. **Design patterns in Petri net system modeling**
Naedele, M.; Janneck, J.W.;
Engineering of Complex Computer Systems, 1998. ICECCS '98. Proceedings. International Conference on
10-14 Aug. 1998 Page(s):47 - 54
Digital Object Identifier 10.1109/ICECCS.1998.706655

AbstractPlus | Full Text: PDF(84 KB) IEEE CNF

42. **A software architecture for multi-criteria decision making for advanced r**
Kapoor, C.; Cetin, M.; Pryor, M.; Cocca, C.; Harden, T.; Tesar, D.;
Intelligent Control (ISIC), 1998. Held jointly with IEEE International Symposiur
Computational Intelligence in Robotics and Automation (CIRA), Intelligent Syst
Semiotics (ISAS), Proceedings of the 1998 IEEE International Symposium on
14-17 Sept. 1998 Page(s):525 - 530
Digital Object Identifier 10.1109/ISIC.1998.713718
AbstractPlus | Full Text: PDF(600 KB) **IEEE CNF**

43. **Hidden partitioning of a visual feedback-based neuro-controller**
Urban, J.P.; Buessler, J.L.; Gresser, J.;
Knowledge-Based Intelligent Electronic Systems, 1998. Proceedings KES '98.
International Conference on
Volume 3, 21-23 April 1998 Page(s):53 - 60 vol.3
Digital Object Identifier 10.1109/KES.1998.725953
AbstractPlus | Full Text: PDF(804 KB) **IEEE CNF**

44. **Parameter estimation of unknown rigid objects moving freely in non-gra**
stereo vision
Matsuno, F.; Sawada, T.;
Intelligent Robots and Systems, 1998. Proceedings., 1998 IEEE/RSJ Internati
on
Volume 3, 13-17 Oct. 1998 Page(s):1455 - 1460 vol.3
Digital Object Identifier 10.1109/IROS.1998.724795
AbstractPlus | Full Text: PDF(424 KB) **IEEE CNF**

45. **START: an application builder for industrial robotics**
Mazer, E.; Boismain, G.; Des Tuves, J.M.B.; Douillard, Y.; Geoffroy, S.; Dubou
Tounsi, M.; Verdot, F.;
Robotics and Automation, 1998. Proceedings. 1998 IEEE International Confer
Volume 2, 16-20 May 1998 Page(s):1154 - 1159 vol.2
Digital Object Identifier 10.1109/ROBOT.1998.677248
AbstractPlus | Full Text: PDF(540 KB) **IEEE CNF**

46. **Handbook of design automation**
Wharton, D.;
Proceedings of the IEEE
Volume 74, Issue 1, Jan. 1986 Page(s):236 - 237
AbstractPlus | Full Text: PDF(271 KB) **IEEE JNL**

47. **A new architecture for robot control**
Yulun Wang; Butner, S.;
Robotics and Automation. Proceedings. 1987 IEEE International Conference c
Volume 4, Mar 1987 Page(s):664 - 670
AbstractPlus | Full Text: PDF(808 KB) **IEEE CNF**

48. **A VLSI chip architecture for the real-time computation of direct kinematic**
Leung, S.; Shanblatt, M.;
Robotics and Automation. Proceedings. 1986 IEEE International Conference c
Volume 3, Apr 1986 Page(s):1717 - 1722
AbstractPlus | Full Text: PDF(568 KB) **IEEE CNF**

49. **Modular neurocontrollers for reaching movements**
Urban, J.P.; Buessler, J.L.; Gresser, J.;
Systems, Man, and Cybernetics, 1998. 1998 IEEE International Conference or
Volume 2, 11-14 Oct. 1998 Page(s):1714 - 1719 vol.2
Digital Object Identifier 10.1109/ICSMC.1998.728141
AbstractPlus | Full Text: PDF(432 KB) **IEEE CNF**

50. **Adaptive invariant novelty filters**
Szu, H.H.; Messner, R.A.;
Proceedings of the IEEE

Volume 74, Issue 3, March 1986 Page(s):518 - 519
AbstractPlus | Full Text: PDF(340 KB) IEEE JNL

51. **Sensor-based robotic assembly systems: Research and applications in e manufacturing**
Sanderson, A.C.; Perry, G.;
Proceedings of the IEEE
Volume 71, Issue 7, July 1983 Page(s):856 - 871
AbstractPlus | Full Text: PDF(1863 KB) IEEE JNL

52. **VLSI design automation: An introduction**
Feuer, M.;
Proceedings of the IEEE
Volume 71, Issue 1, Jan. 1983 Page(s):5 - 9
AbstractPlus | Full Text: PDF(607 KB) IEEE JNL

53. **Microprocessor technology trends**
Myers, G.J.; Yu, A.Y.C.; House, D.L.;
Proceedings of the IEEE
Volume 74, Issue 12, Dec. 1986 Page(s):1605 - 1622
AbstractPlus | Full Text: PDF(2270 KB) IEEE JNL

54. **Minicomputers-a Profile of tomorrow's component**
Kaenel, R.;
Audio and Electroacoustics, IEEE Transactions on
Volume 18, Issue 4, Dec 1970 Page(s):354 - 379
AbstractPlus | Full Text: PDF(4424 KB) IEEE JNL

55. **Design Aspects of Computer Control in Discrete Manufacturing**
Alford, C.;
Manufacturing Technology, IEEE Transactions on
Volume 2, Issue 2, Dec 1973 Page(s):26 - 36
AbstractPlus | Full Text: PDF(1368 KB) IEEE JNL

56. **The ground surveillance robot (GSR): An autonomous vehicle designed t unknown terrain**
Harmon, S.;
Robotics and Automation, IEEE Journal of [legacy, pre - 1988]
Volume 3, Issue 3, Jun 1987 Page(s):266 - 279
AbstractPlus | Full Text: PDF(1848 KB) IEEE JNL

57. **Back cover**
Communications Magazine, IEEE
Volume 23, Issue 2, Feb 1985 Page(s):0 - 0
AbstractPlus | Full Text: PDF(448 KB) IEEE JNL

58. **Integrating computer architectures into the design of high-performance c**
Jacklin, S.; Leyland, J.; Warmbrodt, W.;
Control Systems Magazine, IEEE
Volume 6, Issue 3, Jun 1986 Page(s):3 - 8
AbstractPlus | Full Text: PDF(760 KB) IEEE JNL

59. **Control engineers workbench--A methodology for microcomputer implen controls**
Sutherland, H.; Sonin, K.;
Control Systems Magazine, IEEE
Volume 5, Issue 1, Feb 1985 Page(s):22 - 26
AbstractPlus | Full Text: PDF(576 KB) IEEE JNL

60. **Out of control**

Control Systems Magazine, IEEE
Volume 6, Issue 1, Feb 1986 Page(s):23 - 23

AbstractPlus | Full Text: PDF(144 KB) IEEE JNL

61. **Some lessons learned using expert systems for process control**
Shirley, R.;
Control Systems Magazine, IEEE
Volume 7, Issue 6, Dec 1987 Page(s):11 - 15

AbstractPlus | Full Text: PDF(584 KB) IEEE JNL

62. **A conceptual framework for ASIC design**
Leung, S.S.; Fisher, P.D.; Shanblatt, M.A.;
Proceedings of the IEEE
Volume 76, Issue 7, July 1988 Page(s):741 - 755
Digital Object Identifier 10.1109/5.7141

AbstractPlus | Full Text: PDF(1388 KB) IEEE JNL

63. **VISTA: visual interpretation system for technical applications -architectu**
Paul, D.; Hattich, W.; Nill, W.; Tatari, S.; Winkler, G.;
Pattern Analysis and Machine Intelligence, IEEE Transactions on
Volume 10, Issue 3, May 1988 Page(s):399 - 407
Digital Object Identifier 10.1109/34.3904

AbstractPlus | Full Text: PDF(1492 KB) IEEE JNL

64. **Architectural strategies for an application-specific synchronous multipro environment**
Catthoor, F.; Rabaey, J.; Goossens, G.; Van Meerbergen, J.L.; Jain, R.; de Ma Vandewalle, J.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Sig IEEE Transactions on
Volume 36, Issue 2, Feb. 1988 Page(s):265 - 284
Digital Object Identifier 10.1109/29.1518

AbstractPlus | Full Text: PDF(1956 KB) IEEE JNL

65. **Implementing neural nets with programmable logic**
Vidal, J.J.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Sig IEEE Transactions on
Volume 36, Issue 7, July 1988 Page(s):1180 - 1190
Digital Object Identifier 10.1109/29.1645

AbstractPlus | Full Text: PDF(1064 KB) IEEE JNL

66. **Using the multistage cube network topology in parallel supercomputers**
Siegel, H.J.; Nation, W.G.; Kruskal, C.P.; Napolitano, L.M., Jr.;
Proceedings of the IEEE
Volume 77, Issue 12, Dec. 1989 Page(s):1932 - 1953
Digital Object Identifier 10.1109/5.48833

AbstractPlus | Full Text: PDF(2064 KB) IEEE JNL

67. **Array processor supercomputers**
Potter, J.L.; Meilander, W.C.;
Proceedings of the IEEE
Volume 77, Issue 12, Dec. 1989 Page(s):1896 - 1914
Digital Object Identifier 10.1109/5.48831

AbstractPlus | Full Text: PDF(1508 KB) IEEE JNL

68. **A systolic neural network architecture for hidden Markov models**
Hwang, J.-N.; Vlontzos, J.A.; Kung, S.-Y.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Sig IEEE Transactions on
Volume 37, Issue 12, Dec. 1989 Page(s):1967 - 1979
Digital Object Identifier 10.1109/29.45543

AbstractPlus | Full Text: PDF(1072 KB) IEEE JNL

69. **Least squares associative memory and a theoretical comparison of its pe**
Murakami, K.; Aibara, T.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 19, Issue 5, Sept.-Oct. 1989 Page(s):1230 - 1234
Digital Object Identifier 10.1109/21.44041
AbstractPlus | Full Text: PDF(480 KB) IEEE JNL

70. **Dynamic tunneling algorithm for global optimization**
Yao, Y.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 19, Issue 5, Sept.-Oct. 1989 Page(s):1222 - 1230
Digital Object Identifier 10.1109/21.44040
AbstractPlus | Full Text: PDF(764 KB) IEEE JNL

71. **CASDA: synthesized graphic design of real-time systems**
Mendelbaum, H.G.; Finkelman, D.;
Computer Graphics and Applications, IEEE
Volume 9, Issue 1, Jan. 1989 Page(s):40 - 46
Digital Object Identifier 10.1109/38.20332
AbstractPlus | Full Text: PDF(488 KB) IEEE JNL

72. **Adaptive image feature prediction and control for visual tracking with a h coordinated camera**
Feddema, J.T.; Lee, C.S.G.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 20, Issue 5, Sept.-Oct. 1990 Page(s):1172 - 1183
Digital Object Identifier 10.1109/21.59979
AbstractPlus | Full Text: PDF(880 KB) IEEE JNL

73. **Developing a decision support system for optimizing automated wafer fa**
Pillai, D.D.;
Components, Hybrids, and Manufacturing Technology, IEEE Transactions on | Trans. on Components, Packaging, and Manufacturing Technology, Part A, B,
Volume 13, Issue 1, Mar 1990 Page(s):94 - 102
Digital Object Identifier 10.1109/33.52856
AbstractPlus | Full Text: PDF(868 KB) IEEE JNL

74. **Information modeling for computerized manufacturing**
Hsu, C.; Rattner, L.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 20, Issue 4, July-Aug. 1990 Page(s):758 - 776
Digital Object Identifier 10.1109/21.105077
AbstractPlus | Full Text: PDF(1740 KB) IEEE JNL

75. **Programming real-time applications with SIGNAL**
LeGuernic, P.; Gautier, T.; Le Borgne, M.; Le Maire, C.;
Proceedings of the IEEE
Volume 79, Issue 9, Sept. 1991 Page(s):1321 - 1336
Digital Object Identifier 10.1109/5.97301
AbstractPlus | Full Text: PDF(1144 KB) IEEE JNL

76. **The synchronous approach to reactive and real-time systems**
Benveniste, A.; Berry, G.;
Proceedings of the IEEE
Volume 79, Issue 9, Sept. 1991 Page(s):1270 - 1282
Digital Object Identifier 10.1109/5.97297
AbstractPlus | Full Text: PDF(1000 KB) IEEE JNL

77. **The Chimera II real-time operating system for advanced sensor-based co applications**

Stewart, D.B.; Schmitz, D.E.; Khosla, P.K.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 22, Issue 6, Nov.-Dec. 1992 Page(s):1282 - 1295
Digital Object Identifier 10.1109/21.199456

AbstractPlus | Full Text: PDF(1388 KB) IEEE JNL

78. **Object-oriented real-time systems: concepts and examples**
Bihari, T.E.; Gopinath, P.;
Computer
Volume 25, Issue 12, Dec. 1992 Page(s):25 - 32
Digital Object Identifier 10.1109/2.179114

AbstractPlus | Full Text: PDF(892 KB) IEEE JNL

79. **A color vision inspection system for integrated circuit manufacturing**
Barth, M.; Hirayama, D.; Beni, G.; Hackwood, S.;
Semiconductor Manufacturing, IEEE Transactions on
Volume 5, Issue 4, Nov. 1992 Page(s):290 - 301
Digital Object Identifier 10.1109/66.175361

AbstractPlus | Full Text: PDF(964 KB) IEEE JNL

80. **Lockheed environment for automatic programming**
Graves, H.;
Expert, IEEE [see also IEEE Intelligent Systems and Their Applications]
Volume 7, Issue 6, Dec. 1992 Page(s):15 - 25
Digital Object Identifier 10.1109/64.180405

AbstractPlus | Full Text: PDF(1412 KB) IEEE JNL

81. **Using VHDL for high-level, mixed-mode system simulation**
Srivastava, M.B.; Brodersen, R.W.;
Design & Test of Computers, IEEE
Volume 9, Issue 3, Sept. 1992 Page(s):31 - 40
Digital Object Identifier 10.1109/54.156156

AbstractPlus | Full Text: PDF(960 KB) IEEE JNL

82. **Control primitives for robot systems**
Murray, R.M.; Deno, D.C.; Pister, K.S.J.; Sastry, S.S.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 22, Issue 1, Jan.-Feb. 1992 Page(s):183 - 193
Digital Object Identifier 10.1109/21.141324

AbstractPlus | Full Text: PDF(1148 KB) IEEE JNL

83. **Toward intelligent flight control**
Stengel, R.F.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 23, Issue 6, Nov.-Dec. 1993 Page(s):1699 - 1717
Digital Object Identifier 10.1109/21.257764

AbstractPlus | Full Text: PDF(1900 KB) IEEE JNL

84. **VLSI architectures for polygon recognition**
Sastry, R.; Ranganathan, N.; Bunke, H.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 1, Issue 4, Dec. 1993 Page(s):398 - 407
Digital Object Identifier 10.1109/92.250186

AbstractPlus | Full Text: PDF(844 KB) IEEE JNL

85. **Stability and control of fuzzy dynamic systems via cell-state transitions i hypercubes**
Hoon Kang;
Fuzzy Systems, IEEE Transactions on
Volume 1, Issue 4, Nov. 1993 Page(s):267 - 279
Digital Object Identifier 10.1109/91.251927

AbstractPlus | Full Text: PDF(908 KB) IEEE JNL

86. **Codesign and concurrent engineering**
Buchenrieder, K.;
Computer
Volume 26, Issue 1, Jan. 1993 Page(s):85 - 86
Digital Object Identifier 10.1109/2.179167
AbstractPlus | Full Text: PDF(240 KB) IEEE JNL

87. **Stochastic dynamic programming applied to planning of robot grinding t**
Brown, N.L.; Whitney, D.E.;
Robotics and Automation, IEEE Transactions on
Volume 10, Issue 5, Oct. 1994 Page(s):594 - 604
Digital Object Identifier 10.1109/70.326565
AbstractPlus | Full Text: PDF(972 KB) IEEE JNL

88. **Smarter computer-aided design**
Reinschmidt, K.F.; Finn, G.A.;
Expert, IEEE [see also IEEE Intelligent Systems and Their Applications]
Volume 9, Issue 4, Aug. 1994 Page(s):50 - 55
Digital Object Identifier 10.1109/64.336149
AbstractPlus | Full Text: PDF(732 KB) IEEE JNL

89. **A real-time MPEG encoder using a programmable processor**
Kim, D.; Young, J.; Milton, S.; Kim, H.J.; Kim, Y.;
Consumer Electronics, IEEE Transactions on
Volume 40, Issue 2, May 1994 Page(s):161 - 170
Digital Object Identifier 10.1109/30.286411
AbstractPlus | Full Text: PDF(648 KB) IEEE JNL

90. **Software engineering for semiconductor manufacturing equipment suppl**
Baudoin, C.R.; Kantor, J.P.;
Components, Packaging, and Manufacturing Technology, Part A, IEEE Transa
also Components, Hybrids, and Manufacturing Technology, IEEE Transaction
Volume 17, Issue 2, June 1994 Page(s):230 - 243
Digital Object Identifier 10.1109/95.296404
AbstractPlus | Full Text: PDF(1512 KB) IEEE JNL

91. **Beyond the Fifth Generation: parallel AI research in Japan**
Hendler, J.;
Expert, IEEE [see also IEEE Intelligent Systems and Their Applications]
Volume 9, Issue 1, Feb. 1994 Page(s):2 - 7
Digital Object Identifier 10.1109/64.295139
AbstractPlus | Full Text: PDF(640 KB) IEEE JNL

92. **On-line learning with minimal degradation in feedforward networks**
de Angulo, V.R.; Torras, C.;
Neural Networks, IEEE Transactions on
Volume 6, Issue 3, May 1995 Page(s):657 - 668
Digital Object Identifier 10.1109/72.377971
AbstractPlus | Full Text: PDF(1096 KB) IEEE JNL

93. **The ANDECS CACE framework**
Grubel, G.;
Control Systems Magazine, IEEE
Volume 15, Issue 2, April 1995 Page(s):8 - 13
Digital Object Identifier 10.1109/37.375278
AbstractPlus | Full Text: PDF(512 KB) IEEE JNL

94. **The M*-OBJECT methodology for information system design in CIM envi**
Berio, G.; Di Leva, A.; Giolito, P.; Vernadat, F.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 25, Issue 1, Jan. 1995 Page(s):68 - 85

Digital Object Identifier 10.1109/21.362964

AbstractPlus | Full Text: PDF(1784 KB) IEEE JNL

95. **Neighborhood sequential and random training techniques for CMAC**
Thompson, D.E.; Sunggyu Kwon;
Neural Networks, IEEE Transactions on
Volume 6, Issue 1, Jan. 1995 Page(s):196 - 202
Digital Object Identifier 10.1109/72.363437

AbstractPlus | Full Text: PDF(676 KB) IEEE JNL

96. **Fast neural net simulation with a DSP processor array**
Muller, U.A.; Gunzinger, A.; Guggenbuhl, W.;
Neural Networks, IEEE Transactions on
Volume 6, Issue 1, Jan. 1995 Page(s):203 - 213
Digital Object Identifier 10.1109/72.363436

AbstractPlus | Full Text: PDF(1044 KB) IEEE JNL

97. **Research outpost beyond the sky [International Space Station]**
Dooling, D.;
Spectrum, IEEE
Volume 32, Issue 10, Oct. 1995 Page(s):28 - 33
Digital Object Identifier 10.1109/6.464307

AbstractPlus | Full Text: PDF(712 KB) IEEE JNL

98. **CASM: a VLSI chip for approximate string matching**
Sastry, R.; Ranganathan, N.; Remedios, K.;
Pattern Analysis and Machine Intelligence, IEEE Transactions on
Volume 17, Issue 8, Aug. 1995 Page(s):824 - 830
Digital Object Identifier 10.1109/34.400575

AbstractPlus | References | Full Text: PDF(624 KB) IEEE JNL

99. **New systolic array implementation of the 2-D discrete cosine transform a**
Yu-Tai Chang; Chin-Liang Wang;
Circuits and Systems for Video Technology, IEEE Transactions on
Volume 5, Issue 2, April 1995 Page(s):150 - 157
Digital Object Identifier 10.1109/76.388063

AbstractPlus | Full Text: PDF(640 KB) IEEE JNL

100. **A domain-specific software architecture for adaptive intelligent systems**
Hayes-Roth, B.; Pfleger, K.; Lalanda, P.; Morignot, P.; Balabanovic, M.;
Software Engineering, IEEE Transactions on
Volume 21, Issue 4, April 1995 Page(s):288 - 301
Digital Object Identifier 10.1109/32.385968

AbstractPlus | References | Full Text: PDF(1476 KB) IEEE JNL

Results for "((robots<and>simulation)<and>dataflow) <and> (pyr >= 1951 <and> pyr <=..."

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1. **SIERA: a unified framework for rapid-prototyping of system-level hardwa**
Srivastava, M.; Brodersen, R.W.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction:
Volume 14, Issue 6, June 1995 Page(s):676 - 693
Digital Object Identifier 10.1109/43.387729
AbstractPlus | Full Text: PDF(1876 KB) IEEE JNL

2. **1997 Index Proceedings Of The IEEE Vols. 83-85**
Proceedings of the IEEE
Volume 85, Issue 12, Dec. 1997 Page(s):1 - 47
Digital Object Identifier 10.1109/JPROC.1997.650187
AbstractPlus | Full Text: PDF(1884 KB) IEEE JNL

3. **1996 Index Proceedings of the IEEE Vol. 82-84**
Proceedings of the IEEE
Volume 84, Issue 12, Dec. 1996 Page(s):0_5
Digital Object Identifier 10.1109/JPROC.1996.546442
AbstractPlus | Full Text: PDF(5516 KB) IEEE JNL

4. **The ADAM design planning engine**
Knapp, D.W.; Parker, A.C.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction:
Volume 10, Issue 7, July 1991 Page(s):829 - 846
Digital Object Identifier 10.1109/43.87595
AbstractPlus | Full Text: PDF(1712 KB) IEEE JNL

5. **Using VHDL for high-level, mixed-mode system simulation**
Srivastava, M.B.; Brodersen, R.W.;
Design & Test of Computers, IEEE
Volume 9, Issue 3, Sept. 1992 Page(s):31 - 40
Digital Object Identifier 10.1109/54.156156
AbstractPlus | Full Text: PDF(960 KB) IEEE JNL

6. **Modeling concurrent software**
Sanden, B.;
Software, IEEE
Volume 14, Issue 5, Sept.-Oct. 1997 Page(s):93 - 100
Digital Object Identifier 10.1109/52.605936
AbstractPlus | References | Full Text: PDF(568 KB) IEEE JNL

7. **Implementation of very large dataflow graphs on a reconfigurable archite**

application
Jean-Pierre, D.; Jean-Didier, L.; Tony, P.; Paul, F.;
Parallel and Distributed Processing Symposium., Proceedings 15th Internation
23-27 April 2001 Page(s):1450 - 1456
AbstractPlus | Full Text: PDF(384 KB) IEEE CNF

8. **A Petri net based visual programming language**
Usher, M.; Jackson, D.;
Systems, Man, and Cybernetics, 1998. 1998 IEEE International Conference or
Volume 1, 11-14 Oct 1998 Page(s):107 - 112 vol.1
Digital Object Identifier 10.1109/ICSMC.1998.725393
AbstractPlus | Full Text: PDF(740 KB) IEEE CNF

9. **Computers for symbolic processing**
Wah, B.W.; Lowrie, M.B.; Li, G.-J.;
Proceedings of the IEEE
Volume 77, Issue 4, April 1989 Page(s):509 - 540
Digital Object Identifier 10.1109/5.24142
AbstractPlus | Full Text: PDF(3060 KB) IEEE JNL

10. **System level hardware module generation**
Srivastava, M.B.; Brodersen, R.W.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 3, Issue 1, March 1995 Page(s):20 - 35
Digital Object Identifier 10.1109/92.365451
AbstractPlus | Full Text: PDF(1732 KB) IEEE JNL

11. **Virtual instrumentation and virtual environments**
Spoelder, H.J.W.;
Instrumentation & Measurement Magazine, IEEE
Volume 2, Issue 3, Sep 1999 Page(s):14 - 19
Digital Object Identifier 10.1109/5289.783107
AbstractPlus | Full Text: PDF(472 KB) IEEE JNL

12. **An integrated CAD system for algorithm-specific IC design**
Shung, C.B.; Jain, R.; Rimey, K.; Wang, E.; Srivastava, M.B.; Richards, B.C.; I
Azim, S.; Thon, L.; Hilfinger, P.N.; Rabaey, J.M.; Brodersen, R.W.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction
Volume 10, Issue 4, April 1991 Page(s):447 - 463
Digital Object Identifier 10.1109/43.75628
AbstractPlus | Full Text: PDF(2116 KB) IEEE JNL

13. **Parallel processing for real-time simulation: a case study**
Zomaya, A.Y.;
Parallel & Distributed Technology: Systems & Applications, IEEE [see also IEE
Volume 4, Issue 2, Summer 1996 Page(s):49 - 62
Digital Object Identifier 10.1109/88.494604
AbstractPlus | References | Full Text: PDF(2612 KB) IEEE JNL

14. **Sensor explication: knowledge-based robotic plan execution through log**
Budenske, J.; Gini, M.;
Systems, Man and Cybernetics, Part B, IEEE Transactions on
Volume 27, Issue 4, Aug. 1997 Page(s):611 - 625
Digital Object Identifier 10.1109/3477.604104
AbstractPlus | References | Full Text: PDF(240 KB) IEEE JNL

15. **Author Index**
Supercomputing, 1995. Proceedings of the IEEE/ACM SC95 Conference
1995 Page(s):ii - ii
AbstractPlus | Full Text: PDF(120 KB) IEEE CNF

16. **Book alert**
Proceedings of the IEEE
Volume 73, Issue 3, March 1985 Page(s):495 - 495
AbstractPlus | Full Text: PDF(127 KB) IEEE JNL

17. **A pipelined architecture for parallel image relaxation operations**
Wei Wang; Jun Gu; Henderson, T.;
Circuits and Systems, IEEE Transactions on
Volume 34, Issue 11, Nov 1987 Page(s):1375 - 1384
AbstractPlus | Full Text: PDF(1320 KB) IEEE JNL

18. **The role of process abstraction in simulation**
Fishwick, P.A.;
Systems, Man and Cybernetics, IEEE Transactions on
Volume 18, Issue 1, Jan.-Feb. 1988 Page(s):18 - 39
Digital Object Identifier 10.1109/21.87052
AbstractPlus | Full Text: PDF(2004 KB) IEEE JNL

19. **Implementing neural nets with programmable logic**
Vidal, J.J.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Sig
IEEE Transactions on
Volume 36, Issue 7, July 1988 Page(s):1180 - 1190
Digital Object Identifier 10.1109/29.1645
AbstractPlus | Full Text: PDF(1064 KB) IEEE JNL

20. **Using the multistage cube network topology in parallel supercomputers**
Siegel, H.J.; Nation, W.G.; Kruskal, C.P.; Napolitano, L.M., Jr.;
Proceedings of the IEEE
Volume 77, Issue 12, Dec. 1989 Page(s):1932 - 1953
Digital Object Identifier 10.1109/5.48833
AbstractPlus | Full Text: PDF(2064 KB) IEEE JNL

21. **CASDA: synthesized graphic design of real-time systems**
Mendelbaum, H.G.; Finkelman, D.;
Computer Graphics and Applications, IEEE
Volume 9, Issue 1, Jan. 1989 Page(s):40 - 46
Digital Object Identifier 10.1109/38.20332
AbstractPlus | Full Text: PDF(488 KB) IEEE JNL

22. **Computing as a discipline**
Denning, P.J.; Comer, D.E.; Gries, D.; Mulder, M.C.; Tucker, A.; Turner, A.J.;
Computer
Volume 22, Issue 2, Feb. 1989 Page(s):63 - 70
Digital Object Identifier 10.1109/2.19833
AbstractPlus | Full Text: PDF(712 KB) IEEE JNL

23. **A `Notion' for interactive behavioral animation control**
Wilhelms, J.; Skinner, R.;
Computer Graphics and Applications, IEEE
Volume 10, Issue 3, May 1990 Page(s):14 - 22
Digital Object Identifier 10.1109/38.55148
AbstractPlus | Full Text: PDF(972 KB) IEEE JNL

24. **Frameworks for developing intelligent systems: The ABE systems engin environment**
Hayes-Roth, F.; Davidson, J.E.; Erman, L.D.; Lark, J.S.;
Expert, IEEE [see also IEEE Intelligent Systems and Their Applications]
Volume 6, Issue 3, June 1991 Page(s):30 - 40
Digital Object Identifier 10.1109/64.87682
AbstractPlus | Full Text: PDF(1332 KB) IEEE JNL

25. **User-interface developments for the nineties**
Marcus, A.; van Dam, A.;
Computer
Volume 24, Issue 9, Sept. 1991 Page(s):49 - 57
Digital Object Identifier 10.1109/2.84899
AbstractPlus | Full Text: PDF(1116 KB) **IEEE JNL**

26. **Ada and real-time robotics: lessons learned**
Steele, R.D.; Backes, P.G.;
Computer
Volume 27, Issue 4, April 1994 Page(s):49 - 54
Digital Object Identifier 10.1109/2.275001
AbstractPlus | Full Text: PDF(1260 KB) **IEEE JNL**

27. **Hardware-software co-design of embedded systems [and prolog]**
Wolf, W.H.;
Proceedings of the IEEE
Volume 82, Issue 7, July 1994 Page(s):967 - 989
Digital Object Identifier 10.1109/5.293155
AbstractPlus | Full Text: PDF(2392 KB) **IEEE JNL**

28. **Beyond the Fifth Generation: parallel AI research in Japan**
Hendler, J.;
Expert, IEEE [see also IEEE Intelligent Systems and Their Applications]
Volume 9, Issue 1, Feb. 1994 Page(s):2 - 7
Digital Object Identifier 10.1109/64.295139
AbstractPlus | Full Text: PDF(640 KB) **IEEE JNL**

29. **Computer-aided hardware-software codesign**
De Micheli, G.;
Micro, IEEE
Volume 14, Issue 4, Aug. 1994 Page(s):10 - 16
Digital Object Identifier 10.1109/40.296153
AbstractPlus | Full Text: PDF(680 KB) **IEEE JNL**

30. **Synthesis steps and design models for codesign**
Ismail, T.B.; Jerraya, A.A.;
Computer
Volume 28, Issue 2, Feb. 1995 Page(s):44 - 53
Digital Object Identifier 10.1109/2.347999
AbstractPlus | References | Full Text: PDF(868 KB) **IEEE JNL**

31. **From real-time emulation to ASIC integration for image processing appli**
Kraljic, I.C.; Quenot, G.M.; Zavidovique, B.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 4, Issue 3, Sept. 1996 Page(s):391 - 404
Digital Object Identifier 10.1109/92.532039
AbstractPlus | References | Full Text: PDF(1600 KB) **IEEE JNL**

32. **AI models for business process reengineering**
Yu, E.S.K.; Mylopoulos, J.; Lesperance, Y.;
Expert, IEEE [see also IEEE Intelligent Systems and Their Applications]
Volume 11, Issue 4, Aug. 1996 Page(s):16 - 23
Digital Object Identifier 10.1109/64.511773
AbstractPlus | References | Full Text: PDF(852 KB) **IEEE JNL**

33. **System design methodologies: aiming at the 100 h design cycle**
Gajski, D.D.; Narayan, S.; Ramachandran, L.; Vahid, F.; Fung, P.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 4, Issue 1, March 1996 Page(s):70 - 82
Digital Object Identifier 10.1109/92.486082

AbstractPlus | References | Full Text: PDF(1656 KB) IEEE JNL

34. **Aspects of systems and circuits for nanoelectronics**
Goser, K.F.; Pacha, C.; Kanstein, A.; Rossmann, M.L.;
Proceedings of the IEEE
Volume 85, Issue 4, April 1997 Page(s):558 - 573
Digital Object Identifier 10.1109/5.573741

AbstractPlus | References | Full Text: PDF(288 KB) IEEE JNL

35. **Specification of realtime systems using ASTRAL**
Coen-Porisini, A.; Ghezzi, C.; Kemmerer, R.A.;
Software Engineering, IEEE Transactions on
Volume 23, Issue 9, Sept. 1997 Page(s):572 - 598
Digital Object Identifier 10.1109/32.629494

AbstractPlus | References | Full Text: PDF(400 KB) IEEE JNL

36. **Codesign of embedded systems: status and trends**
Ernst, R.;
Design & Test of Computers, IEEE
Volume 15, Issue 2, April-June 1998 Page(s):45 - 54
Digital Object Identifier 10.1109/54.679207

AbstractPlus | References | Full Text: PDF(108 KB) IEEE JNL

37. **Designing and programming the emotion engine**
Oka, M.; Suzuoki, M.;
Micro, IEEE
Volume 19, Issue 6, Nov.-Dec. 1999 Page(s):20 - 28
Digital Object Identifier 10.1109/40.809374

AbstractPlus | References | Full Text: PDF(472 KB) IEEE JNL

38. **A model-based approach to self-adaptive software**
Karsai, G.; Sztipanovits, J.;
Intelligent Systems and Their Applications, IEEE [see also IEEE Intelligent Sys
Volume 14, Issue 3, May-June 1999 Page(s):46 - 53
Digital Object Identifier 10.1109/5254.769884

AbstractPlus | References | Full Text: PDF(1508 KB) IEEE JNL

39. **On the history of computer science, computer engineering, and compute development in Slovakia**
Dujnic, J.; Fristacky, N.; Molnar, L.; Plander, I.; Rovan, B.;
Annals of the History of Computing, IEEE
Volume 21, Issue 3, July-Sept. 1999 Page(s):38 - 48
Digital Object Identifier 10.1109/85.778981

AbstractPlus | References | Full Text: PDF(728 KB) IEEE JNL

40. **Exception handling in the spreadsheet paradigm**
Burnett, M.; Agrawal, A.; van Zee, P.;
Software Engineering, IEEE Transactions on
Volume 26, Issue 10, Oct. 2000 Page(s):923 - 942
Digital Object Identifier 10.1109/32.879817

AbstractPlus | References | Full Text: PDF(2720 KB) IEEE JNL

41. **Incremental design of a power transformer station controller using a con methodology**
Marchand, H.; Samaan, M.;
Software Engineering, IEEE Transactions on
Volume 26, Issue 8, Aug. 2000 Page(s):729 - 741
Digital Object Identifier 10.1109/32.879811

AbstractPlus | References | Full Text: PDF(1176 KB) IEEE JNL

42. **A three-dimensional human agent metaphor for modeling and simulation**
Hopkins, J.F.; Fishwick, P.A.;

Proceedings of the IEEE
Volume 89, Issue 2, Feb 2001 Page(s):131 - 147
Digital Object Identifier 10.1109/5.910850

AbstractPlus | References | Full Text: PDF(268 KB) | Full Text: HTML IEEE

43. **Telerobot control and real-time simulation environment using parallel pr**
Dodds, G.; Ogasawara, T.; Glover, N.; Kitagaki, K.;
Control Theory and Applications, IEE Proceedings-
Volume 143, Issue 6, Nov. 1996 Page(s):543 - 550

AbstractPlus | Full Text: PDF(800 KB) IEE JNL

44. **Review of modelling techniques and tools for decision making in manufa management**
Pandya, K.V.;
Science, Measurement and Technology, IEE Proceedings-
Volume 142, Issue 5, Sept. 1995 Page(s):371 - 377

AbstractPlus | Full Text: PDF(496 KB) IEE JNL

45. **Autosub: an autonomous unmanned submersible for ocean data collecti**
Collar, P.G.; McPhail, S.D.;
Electronics & Communication Engineering Journal
Volume 7, Issue 3, June 1995 Page(s):105 - 114

AbstractPlus | Full Text: PDF(688 KB) IEE JNL

46. **Proceedings 15th International Parallel and Distributed Processing Symp 2001**
Parallel and Distributed Processing Symposium., Proceedings 15th Internation
23-27 April 2001
Digital Object Identifier 10.1109/IPDPS.2001.924926

AbstractPlus | Full Text: PDF(620 KB) IEEE CNF

47. **A constraint-based controller for soccer-playing robots**
Yu Zhang; Mackworth, A.K.;
Intelligent Robots and Systems, 1998. Proceedings., 1998 IEEE/RSJ Internati on
Volume 2, 13-17 Oct. 1998 Page(s):1290 - 1295 vol.2
Digital Object Identifier 10.1109/IROS.1998.727477

AbstractPlus | Full Text: PDF(492 KB) IEEE CNF

48. **A concurrent visual language based on Petri nets**
Usher, M.; Jackson, D.;
Visual Languages, 1998. Proceedings. 1998 IEEE Symposium on
1-4 Sept. 1998 Page(s):72 - 73
Digital Object Identifier 10.1109/VL.1998.706139

AbstractPlus | Full Text: PDF(36 KB) IEEE CNF

49. **A general specification for scene animation**
Lee, G.S.;
Computer Graphics, Image Processing, and Vision, 1998. Proceedings. SIBGI International Symposium on
20-23 Oct. 1998 Page(s):216 - 223
Digital Object Identifier 10.1109/SIBGRA.1998.722753

AbstractPlus | Full Text: PDF(84 KB) IEEE CNF

50. **Hardware of structured brain computer**
Ae, T.; Sakai, K.; Araki, H.; Honda, N.;
Knowledge-Based Intelligent Electronic Systems, 1998. Proceedings KES '98. International Conference on
Volume 3, 21-23 April 1998 Page(s):533 - 540 vol.3
Digital Object Identifier 10.1109/KES.1998.726019

AbstractPlus | Full Text: PDF(464 KB) IEEE CNF

51. **IEEE. APCCAS 1998. 1998 IEEE Asia-Pacific Conference on Circuits and : Microelectronics and Integrating Systems. Proceedings (Cat. No.98EX24;**
Circuits and Systems, 1998. IEEE APCCAS 1998. The 1998 IEEE Asia-Pacific
24-27 Nov. 1998
Digital Object Identifier 10.1109/APCCAS.1998.743642
AbstractPlus | Full Text: PDF(932 KB) **IEEE CNF**

52. **Proceedings of the IEEE/ACM SC95 Conference - Table of Contents**
Supercomputing, 1995. Proceedings of the IEEE/ACM SC95 Conference
1995 Page(s):i - i
AbstractPlus | Full Text: PDF(89 KB) **IEEE CNF**

53. **Evaluation of fault tolerant software structures for parallel systems in inc**
Tyrrell, A.M.; Sillitoe, I.P.W.;
Control 1991. Control '91., International Conference on
25-28 Mar 1991 Page(s):393 - 398 vol.1
AbstractPlus | Full Text: PDF(384 KB) **IEE CNF**

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Design of **dynamically reconfigurable real-time software** using port-based objects
DB Stewart, RA Volpe, PK Khosla - IEEE Transactions on Software Engineering, 1997 - shum.huji.ac.il
Page 1. Design of **Dynamically Reconfigurable Real- Time Software** Using Port-Based Objects IEEE Transactions on Software Engineering, Vol. 23, No. **...**
Cited by 150 - View as HTML - Web Search - telerobotics.jpl.nasa.gov - portal.acm.org - ri.cmu.edu - all 12 versions »

Chimera methodology: designing **dynamically reconfigurable real-time software** using port-based ...
DB Stewart, PK Khosla - The the 1994 1 st Workshop on Object-Oriented Real-Time ..., 1994 - doi.ieeecs.org
Page 1. The Chimera Methodology: Designing **Dynamically Reconfigurable Real-Time Software** using Port-Based Objects David B. Stewart* and PK Khosla+ *Dept. **...**
Cited by 6 - Web Search - doi.ieeecomputersociety.org - ieeexplore.ieee.org - ee.umd.edu - all 13 versions »

... in Mission-Critical Systems: **Dynamically Reconfigurable Real-Time Software** for Flight Control ...
DB Stewart, RA Brown - Workshop on Real-Time Mission-Critical Systems in ..., 1999 - ee.umd.edu
... for Real-Time Systems (SERTS) Laboratory at University of Maryland have teamed up to design **dynamically reconfigurable real- time software** for autonomous **...**
Cited by 3 - View as HTML - Web Search - enee.umd.edu - ece.umd.edu - embedded-zone.com - all 5 versions »

[CITATION] An O (1) Distributed Exception Handling Mechanism for **Dynamically Reconfigurable Real-Time Software** ...
J Lang - Maryland: University of Maryland, 1997
Cited by 2 - Web Search

[CITATION] Design of **Dynamically Reconfigurable Real-time Software** Using Port-Based Objects
D Steward, R Volpe, P Khosla - IEEE Transactions on Software Engineering, 1997
Cited by 2 - Web Search

[CITATION] An O (1) distributed exception handling mechanism for **dynamically reconfigurable real-time software**
J Lang - 1997 - research directed by Electrical Engineering Dept.University ...
Cited by 1 - Web Search

Dynamically reconfigurable real-time software components in the RTLinux environment
K Valtanen, T Nayha - Proceedings of SPIE, 2000 - adsabs.harvard.edu
Title: **Dynamically reconfigurable real-time software** components in the RTLinux environment Authors: Valtanen, Kristiina; Nayha, Tuomo Affiliation: AA(VTT **...**
Web Search - link.aip.org

Design of **Dynamically Reconfigurable Real-Time Software** Using Port-Based Objects
PK Khosla, I Fellow - IEEE TRANSACTIONS ON SOFTWARE ENGINEERING, 1997 - ieeexplore.ieee.org
IEEE TRANSACTIONS ON SOFTWARE ENGINEERING, VOL. 23, NO. 12, DECEMBER 1997 759 Design of **Dynamically Reconfigurable Real-Time Software** Using Port-Based Objects **...**
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2 Research directions in virtual environments: report of an NSF Invitational Workshop, March 23-24, 1992, University of North Carolina at Chapel Hill
Gary Bishop, Henry Fuchs
August 1992 **ACM SIGGRAPH Computer Graphics**, Volume 26 Issue 3
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3 A hybrid numeric/symbolic program for checking functional and timing compatibility of synthesized designs
Chih Tung Chen, Alice C. Parker
May 1994 **Proceedings of the 7th international symposium on High-level synthesis**
Publisher: IEEE Computer Society Press
Full text available: pdf(588.17 KB) Additional Information: full citation, references

4 A modular approach to simulation of robotic systems
D. J. Medeiros, R. P. Sadowski, D. W. Starks, B. S. Smith
January 1980 **Proceedings of the 12th conference on Winter simulation**
Publisher: IEEE Press
Full text available: pdf(397.37 KB) Additional Information: full citation, abstract, references, index terms

This paper presents the results of a simulation effort performed as part of the ICAM robotics program. The initial Q-GERT simulation model is briefly described, followed by the

presentation of a modular approach to simulation of robotic systems. In this approach, predefined Q-GERT modules, described by simple block diagrams, are assembled to produce a complete model. The system design logic is discussed and the available modules are briefly described. Examples are presented in block diagram ...

5 Increasing the power and value of manufacturing simulation via collaboration with other analytical tools (panel session): a panel discussion
Onur M. Ülgen, John Shore, Gene Coffman, David Sly, Matt Rohrer, Demet Wood
December 1999 **Proceedings of the 31st conference on Winter simulation: Simulation---a bridge to the future - Volume 1**
Publisher: ACM Press
Full text available: pdf(62.28 KB) Additional Information: full citation, citings, index terms

6 An approach to using a data base management system in manufacturing system simulation
Hui Jun Han, Kenneth A. Fegley
December 1986 **Proceedings of the 18th conference on Winter simulation**
Publisher: ACM Press
Full text available: pdf(709.81 KB) Additional Information: full citation, abstract, references, index terms

The organization and management of the data in a computer simulation of a discrete event system can be a significant task. This task can be facilitated by linking a data base management system with the computer language used for the simulation. This paper presents such a linkage using dBase III for the data base management system and TURBO-PASCAL for the computer language. dBase provides the means to file system model parameters, simulation input data and simulation output data. Since dBase ...

7 The ControlShell component-based real-time programming system, and its application to the Marsokhod Martian Rover
Stan Schneider, Vincent Chen, Jay Steele, Gerardo Pardo-Castellote
November 1995 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1995 workshop on Languages, compilers, & tools for real-time systems LCTES '95**, Volume 30 Issue 11
Publisher: ACM Press
Full text available: pdf(1.39 MB) Additional Information: full citation, abstract, references, index terms

Real-time system software is notoriously hard to share and reuse. This paper walks through the methodology and application of ControlShell, a component-based programming system for real-time system software development. ControlShell combines graphical system-building tools, an execution-time configuration manager, a real-time matrix package, and an object name service into an integrated development environment. It targets complex systems that require on-line reconfiguration and strategic control ...

8 An integration test-bed system for supply chain management
Shigeki Umeda, Albert Jones
December 1998 **Proceedings of the 30th conference on Winter simulation**
Publisher: IEEE Computer Society Press
Full text available: pdf(97.66 KB) Additional Information: full citation, references, citings, index terms

9 Development of generic simulation models to evaluate wafer fabrication cluster tools
Neal G. Pierce, Michael J. Drevna
December 1992 **Proceedings of the 24th conference on Winter simulation**
Publisher: ACM Press
Full text available: pdf(449.87 KB) Additional Information: full citation, references, citings, index terms

10 A real world object modeling method for creating simulation environment of real-time systems

Ji Y. Lee, Hye J. Kim, Kyo C. Kang

October 2000 **ACM SIGPLAN Notices , Proceedings of the 15th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications OOPSLA '00**, Volume 35 Issue 10

Publisher: ACM Press

Full text available: pdf(405.18 KB) Additional Information: full citation, abstract, references, citings, index terms

Most real-time embedded control software feature complex interactions with asynchronous inputs and environment objects, and a meaningful simulation of a real-time control software specification requires realistic simulation of its environment. Two problems that need to be addressed in the simulation of a target software system and its environment: First, integration and simulation of the specifications of a target software system and its artificial environment are often performed too late in the ...

Keywords: real-time control software, requirement specification, simulation, validation, verification

11 Applying the simulation process: simulation study of HDA parts degreaser

Robert Kittell, Ann Dunkin

December 1991 **Proceedings of the 23rd conference on Winter simulation**

Publisher: IEEE Computer Society

Full text available: pdf(475.07 KB) Additional Information: full citation, references, index terms

12 Future of simulation: Simulation in the international IMS MISSION project: the IMS MISSION architecture for distributed manufacturing simulation

Charles McLean, Frank Riddick

December 2000 **Proceedings of the 32nd conference on Winter simulation**

Publisher: Society for Computer Simulation International

Full text available: pdf(269.47 KB) Additional Information: full citation, abstract, references, citings

This paper presents an overview of a neutral reference architecture for integrating distributed manufacturing simulation systems with each other, with other manufacturing software applications, and with manufacturing data repositories. Other manufacturing software applications include, but are not limited to systems used to: 1) design products, 2) specify processes, 3) engineer manufacturing systems, and 4) manage production. The architecture identifies the software building blocks and interface ...

13 Mobile robot simulation of clinical laboratory deliveries

Manuel D. Rossetti, Amit Kumar, Robin A. Felder

December 1998 **Proceedings of the 30th conference on Winter simulation**

Publisher: IEEE Computer Society Press

Full text available: pdf(108.53 KB) Additional Information: full citation, references, index terms

14 Evaluation of cluster tool throughput for thin film head production

Eric J. Koehler, Timbur M. Wulf, Alvin C. Bruska, Marvin S. Seppanen

December 1999 **Proceedings of the 31st conference on Winter simulation: Simulation--a bridge to the future - Volume 1**

Publisher: ACM Press

Full text available: pdf(83.62 KB) Additional Information: full citation, references, citings, index terms

15 Annotated bibliography of the proceedings of the annual simulation symposium (1968-1991)
Ross A. Gagliano, Martin D. Fraser
April 1992 **Proceedings of the 25th annual symposium on Simulation**
Publisher: IEEE Computer Society Press
Full text available: pdf(1.45 MB) Additional Information: full citation, references, index terms

16 Simulation and advanced manufacturing system design
Van B. Norman, T. A. Norman
December 1986 **Proceedings of the 18th conference on Winter simulation**
Publisher: ACM Press
Full text available: pdf(413.46 KB) Additional Information: full citation, abstract, index terms

A case study is presented which demonstrates the design and implementation of an integrated manufacturing system using AutoMod, an integrated simulation tool developed by AutoSimulations, Inc.

17 Simulation modelling support via network based concepts
Stephen C. Mathewson
December 1990 **Proceedings of the 22nd conference on Winter simulation**
Publisher: IEEE Press
Full text available: pdf(953.01 KB) Additional Information: full citation, references, citings, index terms

18 Implementation of the data-flow synchronous language SIGNAL
Pascalin Amagbégnon, Loïc Besnard, Paul Le Guernic
June 1995 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1995 conference on Programming language design and implementation PLDI '95**, Volume 30 Issue 6
Publisher: ACM Press
Full text available: pdf(1.03 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper presents the techniques used for the compilation of the data-flow, synchronous language SIGNAL. The key feature of the compiler is that it performs formal calculus on systems of boolean equations. The originality of the implementation of the compiler lies in the use of a tree structure to solve the equations.

19 Planning of production and material flow systems by inter-interactive computer graphics simulation
Werner Großeschallau, Robert Heinzel
January 1984 **Proceedings of the 17th annual symposium on Simulation**
Publisher: IEEE Press
Full text available: pdf(747.81 KB) Additional Information: full citation, abstract, references, citings, index terms

The INSIMAS (Interactive Simulation of Material Flow-Systems) simulation system is described. The systems-concept corresponds to the four planning steps design, analysis, simulation and evaluation. Each step is supported by interactive computer graphics procedures. The simulation run can be represented on a graphics screen by moving of objects (vehicles) through the simulated network. Main applicatio ...

20 Real-time hierarchically distributed processing network interaction simulation
Wayne F. Zimmerman, Chung-I Wu
January 1988 **Proceedings of the 21st annual symposium on Simulation**

Publisher: IEEE Computer Society Press
Full text available: pdf(1.26 MB) Additional Information: full citation, abstract, references, index terms

The Telerobot Testbed is a hierarchically distributed processing system which is linked together through a standard, commercial Ethernet. Standard Ethernet systems are primarily designed to manage non-real-time information transfer. Therefore, collisions on the net (i.e., two or more sources attempting to send data at the same time) are managed by randomly rescheduling one of the sources to retransmit at a later time interval. Although acceptable for transmitting noncritical data such as ma ...

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  John Colter, Netscape Navigator
  ```

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